STAFF

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Mario C. Laguzzi, Member of Technical Staff
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REGISTRATION INFORMATION

Seminars will be limited in size. Registrations will be accepted in the order received. Substitution of applicants may be made at any time. Cancellation of registration will be honored and fee refunded provided notification is received no later than one week prior to the seminar. Registration fee includes attendance, luncheon, coffee break, and a complete package of reference material. Reference material will be issued at the opening session. Telephone answering service is provided during seminar hours.

HOTEL ACCOMMODATIONS

RCA Institutes has reserved a block of rooms in each seminar hotel. Hotel room is not included in the registration fee. Additional hotel information will be forwarded with your confirmed reservation. Please arrange room accommodations directly with the hotel, with reference to RCA Institutes' seminar.

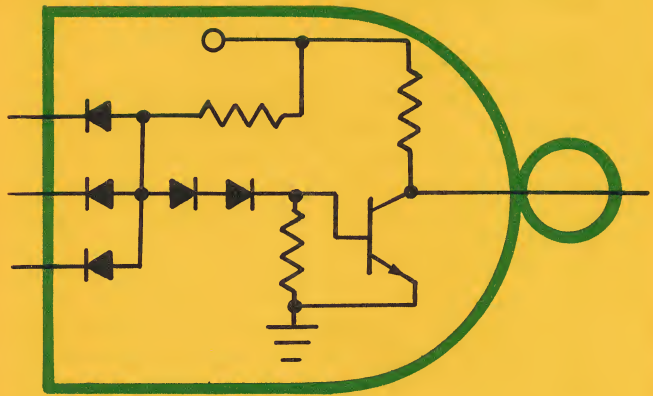## OTHER SEMINARS IN COMPUTER TECHNOLOGY INCLUDE:

Logic Design   Digital Communications
          Digital Systems Engineering

RCA INSTITUTES, Inc.
A Service of Radio Corporation of America
School of Custom Educational Programs
49 West 45th Street
New York, N.Y. 10036
Area Code 212, 247-5700

RCA INSTITUTES
Announces
A Five-Day Seminar—

DIGITAL ELECTRONICS

Digital Electronics

INTRODUCTION

DIGITAL ELECTRONICS is one of a series of seminars on modern technology presented by RCA Institutes. This program has been developed for engineers and other technical personnel faced with the challenging new areas in the theory, design and technical operations of solid-state digital circuits. Its objective is twofold: To equip participants with practical, up-to-date procedures for designing logic packages; and to help participants gain maximum utility and reliability from commercial digital integrated circuits.

SCOPE

Whereas the other programs in this series employ the "Black-Box" approach, this seminar is directed to the internal construction of basic digital building blocks. The program begins with the definition and measurement of important package parameters. Practical design procedures are then developed for diode and transistor gating, and optimum configuration for integrated circuits are analyzed. Finally, applications of field-effect transistors are given. In all development, emphasis is placed on problems of interfacing and noise.

PREREQUISITES

The minimum requirements are a bachelor of science degree in electrical engineering, physics, or the equivalent. A knowledge of D.C. and A.C. circuit theory is essential. Prior knowledge of Boolean Algebra will be helpful but is not required.

PRESENTATION

Members of the School of Custom Educational Programs will present this program of concentrated study using a unique cross-fire teaching technique designed to motivate the participant.

This seminar has been developed through extensive field research by the School of Custom Educational Programs in consultation with RCA Institutes' Board of Technical Advisers, representing various technical, research and educational activities of the Radio Corporation of America and its subsidiaries.

SEMINAR OUTLINE

FIRST DAY

INTRODUCTION AND REVIEW

The package concept; measuring integrated circuit parameters; logic symbology and standards; standard combinational and recursive packages; review of junction characteristics.

SECOND DAY*

SEMICONDUCTOR CIRCUITS

Design of diode gating; transistor behavior and regions of operations, basic circuit configurations; parameter variations; transistor switching circuits.

THIRD DAY*

TRANSISTOR GATE DESIGN

Noise immunity and critical voltages; analysis of the basic inverter; designing modern inverter and driver circuits; designing DTL and RTL gates.

FOURTH DAY*

DESIGN WITH INTEGRATED CIRCUITS

Logic forms for integrated circuits; complementary output; current-mode logic; TTL gating; type RS, T and D flip-flops; universal JK elements; master-slave circuits.

FIFTH DAY*

SPECIAL DEVICES

Clock generators; design of single-shots and Schmitt triggers; interface compatibility and timing; solutions to reliability, and noise problems. Design applications of junction and MOS field-effect devices; tunnel diode applications.

*WORK SESSION

In sessions conducted during class hours, participants will design, construct and test digital circuits on special training equipment.

BRADFORD DAGGETT
DIRECTOR

AREA CODE 212
247-5700

RCA Institutes Announces an Important Seminar --
   DIGITAL ELECTRONICS --
   To Be Presented in New York City

This seminar presents modern design methods for solid-state
digital circuits with emphasis on the integrated "package"
concept. Special attention is given to problems of noise,
compatibility and interfacing.

Intended for Design or Applications Engineers, this presenta-
tion developed through field research, will explore the capabili-
ties and limitations of major circuit configurations. Methods
for designing special circuits when stock packages are not
practical will be given through the use of classroom exercises.
Participants will also use the techniques developed to design
and construct circuitry on breadboards.

        The place: Belmont Plaza Hotel
                        Lexington Avenue at 49th Street

        The dates: March 6 through March 10, 1967
                        9:00 a.m. to 4:30 p.m. daily

We urge you to register promptly, since attendance will be
limited. A folder covering the seminar is enclosed as well as
a registration form. Hotel reservations should be made directly
with the hotel, phone (212) PLaza 5-1200.

Please call or write us, should you have any questions.

                        Sincerely,

                        Bradford Daggett